Solar Panel Checks

Transmitter

15:03:11.105 -> Initalisation Begins

15:03:11.240 -> LoRa radio init OK!

15:03:11.275 -> Set Freq to: 915.00

15:03:11.275 -> Initalisation finished

15:03:11.310 -> satellite launches

15:03:15.879 -> Begin collecting data

15:03:15.914 -> Battery voltage is: 1

15:03:16.876 -> Finish collecting data

15:03:16.910 -> begin Transmit

15:03:17.877 -> Sending: Ident: A81X BV: D1: p D2: p D3: p D4: p

15:03:19.112 -> Transmission complete

15:03:19.112 -> enter receive cycle

15:03:22.127 -> finish receive cycle

15:03:22.127 -> enter idle cycle

15:03:27.523 -> finish idle cycle

15:03:27.557 -> Begin collecting data

15:03:27.592 -> Battery voltage is: 2

15:03:28.554 -> Finish collecting data

15:03:28.588 -> begin Transmit

15:03:29.546 -> Sending: Ident: A81X BV: D1: ⸮ D2: ⸮ D3: ⸮ D4: ⸮

15:03:30.743 -> Transmission complete

15:03:30.777 -> enter receive cycle

15:03:33.770 -> finish receive cycle

15:03:33.805 -> enter idle cycle

15:03:39.204 -> finish idle cycle

15:03:39.204 -> Begin collecting data

15:03:39.238 -> Battery voltage is: 3

15:03:40.202 -> Finish collecting data

15:03:40.237 -> begin Transmit

15:03:41.199 -> Sending: Ident: A81X BV: D1: m D2: m D3: m D4: m

15:03:42.406 -> Transmission complete

15:03:42.441 -> enter receive cycle

15:03:45.455 -> finish receive cycle

15:03:45.455 -> enter idle cycle

15:03:50.847 -> finish idle cycle

15:03:50.882 -> Begin collecting data

15:03:50.915 -> Battery voltage is: 4

15:03:51.843 -> Finish collecting data

15:03:51.877 -> begin Transmit

15:03:52.871 -> Sending: Ident: A81X BV: D1: f D2: f D3: f D4: f

15:03:54.074 -> Transmission complete

15:03:54.108 -> enter receive cycle

15:03:57.101 -> finish receive cycle

15:03:57.101 -> enter idle cycle

15:04:02.520 -> finish idle cycle

15:04:02.520 -> Begin collecting data

15:04:02.554 -> Battery voltage is: 5

15:04:03.518 -> Finish collecting data

15:04:03.552 -> begin Transmit

15:04:04.513 -> Sending: Ident: A81X BV: D1: ⸮ D2: ⸮ D3: ⸮ D4: ⸮

15:04:05.715 -> Transmission complete

15:04:05.749 -> enter receive cycle

15:04:08.740 -> finish receive cycle

15:04:08.775 -> enter idle cycle

15:04:14.161 -> finish idle cycle

15:04:14.194 -> Begin collecting data

15:04:14.228 -> Battery voltage is: 6

15:04:15.191 -> Finish collecting data

15:04:15.191 -> begin Transmit

15:04:16.190 -> Sending: Ident: A81X BV: D1: ⸮ D2: ⸮ D3: ⸮ D4: ⸮

15:04:17.396 -> Transmission complete

15:04:17.431 -> enter receive cycle

15:04:20.410 -> finish receive cycle

15:04:20.445 -> enter idle cycle

15:04:25.813 -> finish idle cycle

15:04:25.847 -> Begin collecting data

15:04:25.882 -> Battery voltage is: 7

15:04:26.847 -> Finish collecting data

15:04:26.847 -> begin Transmit

15:04:27.847 -> Sending: Ident: A81X BV: D1: ⸮ D2: ⸮ D3: ⸮ D4: ⸮

15:04:29.049 -> Transmission complete

15:04:29.084 -> enter receive cycle

15:04:32.074 -> finish receive cycle

15:04:32.109 -> enter idle cycle

15:04:37.503 -> finish idle cycle

15:04:37.503 -> Begin collecting data

15:04:37.538 -> Battery voltage is: 8

15:04:38.505 -> Finish collecting data

15:04:38.539 -> begin Transmit

15:04:39.502 -> Sending: Ident: A81X BV: D1: o D2: o D3: o D4: o

15:04:40.731 -> Transmission complete

15:04:40.731 -> enter receive cycle

15:04:43.724 -> finish receive cycle

15:04:43.758 -> enter idle cycle

15:04:49.153 -> finish idle cycle

15:04:49.188 -> Begin collecting data

15:04:49.223 -> Battery voltage is: 9

15:04:50.154 -> Finish collecting data

15:04:50.187 -> begin Transmit

15:04:51.184 -> Sending: Ident: A81X BV: D1: ⸮ D2: ⸮ D3: ⸮ D4: ⸮

15:04:52.385 -> Transmission complete

15:04:52.385 -> enter receive cycle

15:04:55.404 -> finish receive cycle

15:04:55.404 -> enter idle cycle

15:05:00.835 -> finish idle cycle

15:05:00.835 -> Begin collecting data

15:05:00.870 -> Battery voltage is: 10

15:05:01.833 -> Finish collecting data

15:05:01.867 -> begin Transmit

15:05:02.830 -> Sending: Ident: A81X BV:

15:05:02.864 -> D1: ⸮ D2: ⸮ D3: ⸮ D4: ⸮

15:05:04.040 -> Transmission complete

15:05:04.074 -> enter receive cycle

15:05:07.061 -> finish receive cycle

15:05:07.095 -> enter idle cycle

15:05:12.463 -> finish idle cycle

15:05:12.497 -> Begin collecting data

15:05:12.532 -> Battery voltage is: 11

15:05:13.493 -> Finish collecting data

15:05:13.527 -> begin Transmit

15:05:14.495 -> Sending: Ident: A81X BV:   
 D1: ⸮ D2: ⸮ D3: ⸮ D4: ⸮

15:05:15.695 -> Transmission complete

15:05:15.730 -> enter receive cycle

15:05:18.720 -> finish receive cycle

15:05:18.755 -> enter idle cycle

15:05:24.146 -> finish idle cycle

15:05:24.181 -> Begin collecting data

15:05:24.181 -> Battery voltage is: 12

15:05:25.142 -> Finish collecting data

15:05:25.176 -> begin Transmit

15:05:26.177 -> Sending: Ident: A81X BV: D1: ⸮ D2: ⸮ D3: ⸮ D4: ⸮

15:05:27.377 -> Transmission complete

15:05:27.377 -> enter receive cycle

15:05:30.367 -> finish receive cycle

15:05:30.402 -> enter idle cycle

**Receiver**

15:03:18.081 -> Got: Ident: A81X BV: D1: p D2: p D3: p D4: p

15:03:18.081 -> number of Bytes is: 50

15:03:29.756 -> Got: Ident: A81X BV: D1: ⸮ D2: ⸮ D3: ⸮ D4: ⸮

15:03:29.756 -> number of Bytes is: 50

15:03:41.414 -> Got: Ident: A81X BV: D1: m D2: m D3: m D4: m

15:03:41.414 -> number of Bytes is: 50

15:03:53.051 -> Got: Ident: A81X BV: D1: f D2: f D3: f D4: f

15:03:53.051 -> number of Bytes is: 50

15:04:04.710 -> Got: Ident: A81X BV: D1: ⸮ D2: ⸮ D3: ⸮ D4: ⸮

15:04:04.710 -> number of Bytes is: 50

15:04:16.399 -> Got: Ident: A81X BV: D1: ⸮ D2: ⸮ D3: ⸮ D4: ⸮

15:04:16.399 -> number of Bytes is: 50

15:04:28.035 -> Got: Ident: A81X BV: D1: ⸮ D2: ⸮ D3: ⸮ D4: ⸮

15:04:28.035 -> number of Bytes is: 50

15:04:39.698 -> Got: Ident: A81X BV: D1: o D2: o D3: o D4: o

15:04:39.698 -> number of Bytes is: 50

15:04:51.376 -> Got: Ident: A81X BV: D1: ⸮ D2: ⸮ D3: ⸮ D4: ⸮

15:04:51.376 -> number of Bytes is: 50

15:05:03.037 -> Got: Ident: A81X BV:

15:05:03.037 -> D1: ⸮ D2: ⸮ D3: ⸮ D4: ⸮

15:05:03.037 -> number of Bytes is: 50

15:05:14.681 -> Got: Ident: A81X BV:   
 D1: ⸮ D2: ⸮ D3: ⸮ D4: ⸮

15:05:14.681 -> number of Bytes is: 50

15:05:26.340 -> Got: Ident: A81X BV: D1: ⸮ D2: ⸮ D3: ⸮ D4: ⸮

15:05:26.340 -> number of Bytes is: 50